



Proactive Yield Management using Frequent Pattern Database

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Outline

- Overview
- Motivation
- Technology
- Benefits
- Methodology
- Operational Workflow
- Challenges
- Case Studies
- Conclusion & Future Work
- Acknowledgements

Proactive Yield Management using Frequent Pattern Database



Overview

GOOD

AI based Spatial Pattern Recognition (SPR) system for inline wafer monitoring

BETTER

Performing offline reactive root cause analysis for the detected spatial patterns

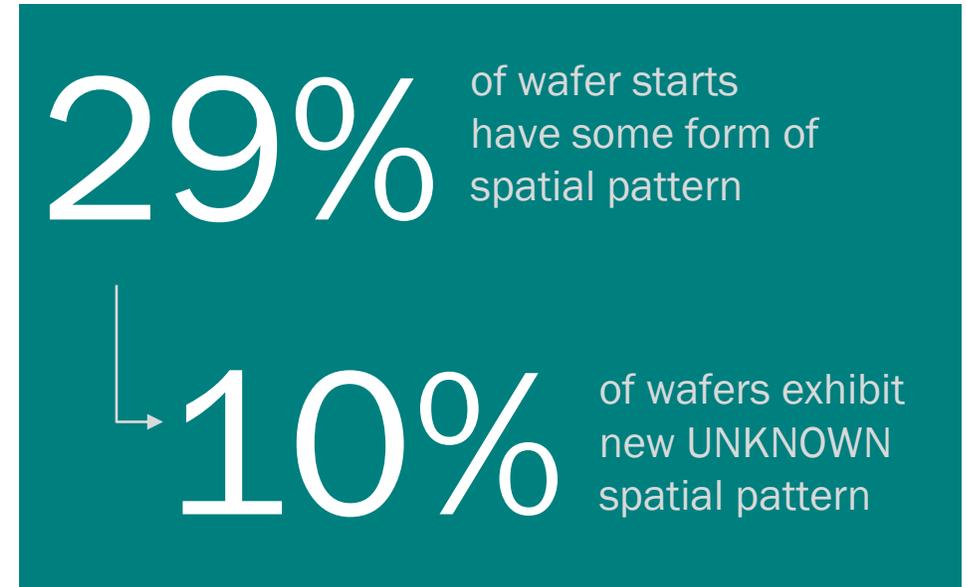
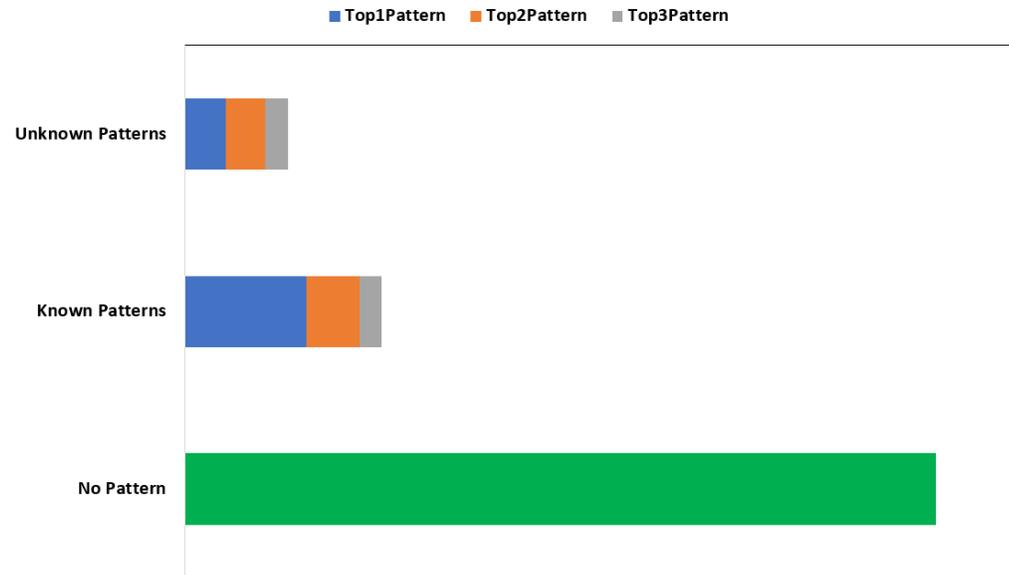
BEST

Proactive association of spatial patterns to WIP process step and equipment

Reactive to Proactive

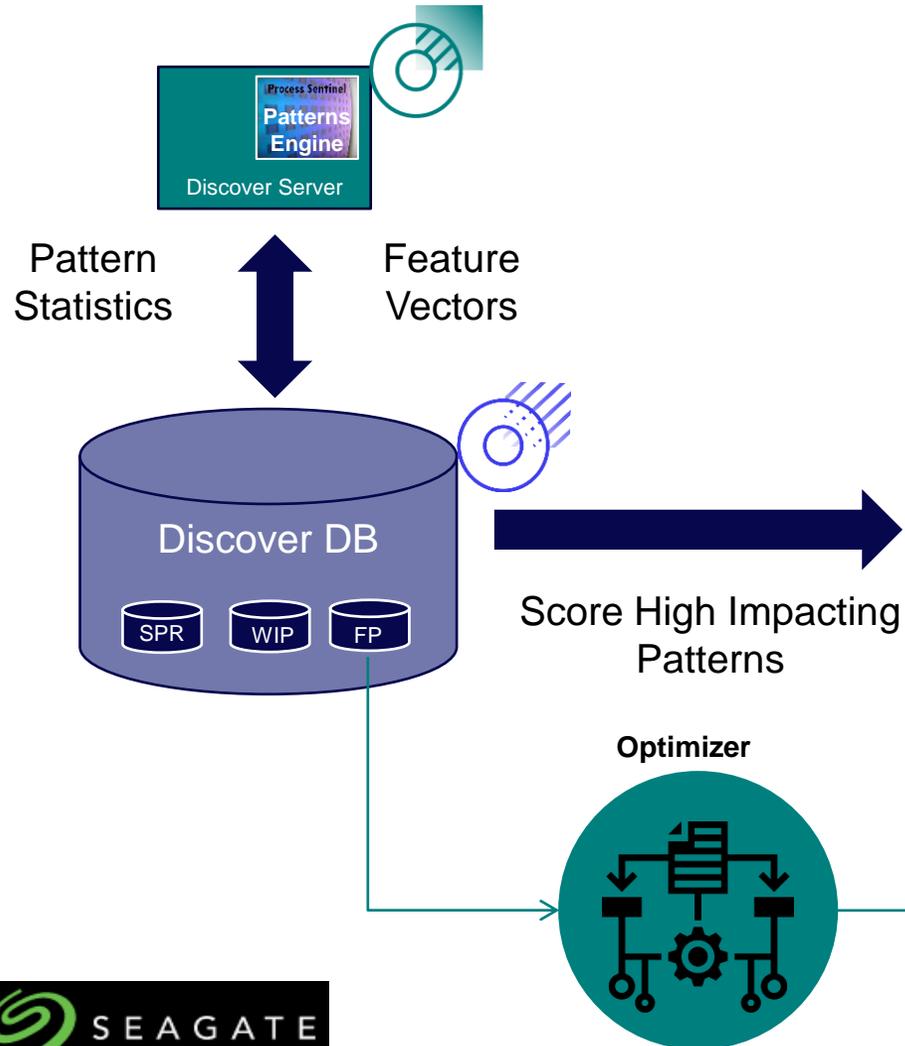


Motivation

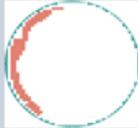


“Analyzing yield loss from both defect and wafer probe data led to the conclusion that *solving systematic spatial pattern challenges in production is the missing link* between inline tool control and yield improvement!”

Technology – Build Frequent Pattern Database



Yield limiting pattern on a wafer is a sign of a systematic issue due to process or tool marginalities!

Rank	Failure Type	Wafer Map	# of Wafers	Failure Rate	Route/Stage : Step	Process Tool
1	Edge Arc		410	0.32	C-MET1ET : CE51B	CELRC01X
2	SP2		350	0.28	C-BCAPCLN : CA085	CAFSI02X
3	Center Core		100	0.10	C-YWDEP2 : CY311	CY310

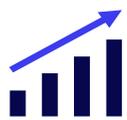
Benefits



**Inline Monitoring &
Lot/Wafer Disposition Strategy**

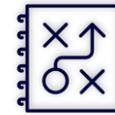


Learning from UNKNOWN



Yield & Productivity Improvement

- Timely Root Cause
- Dashboards for High Impacting Route Step & Tools



Drive actions in production (OCAP)

- Lot Hold, Tool Downs, Rework, Chamber Actions and Test wafer measurements
- Interacting with SPC



Quality Improvement

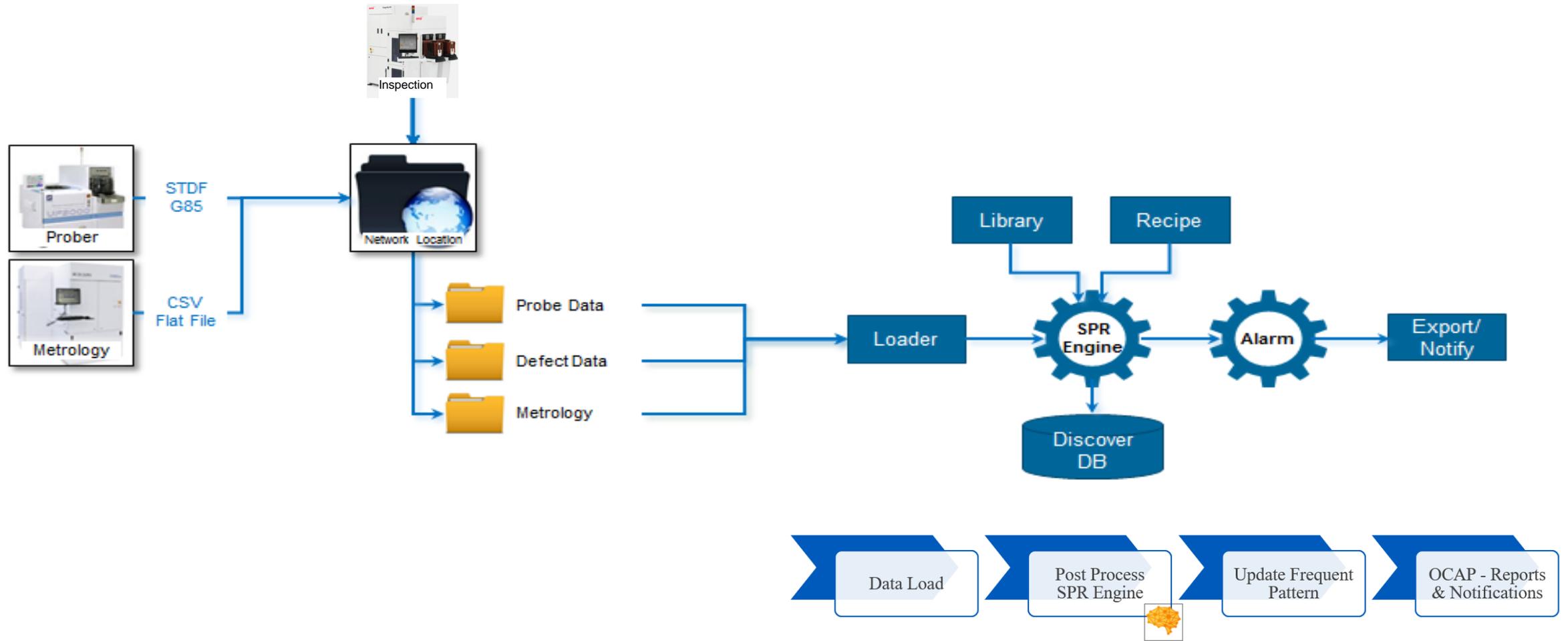
- Proactive Die Binning
- Pattern Search
- Event Scope

Introduction / Methodology

Six months of AOI & Wafer Probe production data was used.

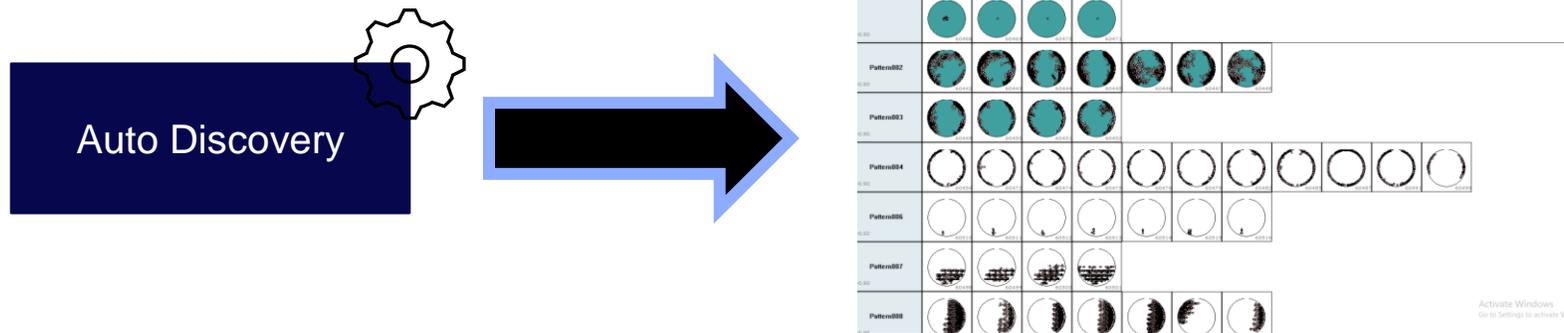


Discover Patterns Operational Workflow



Learning from the “UNKNOWN”

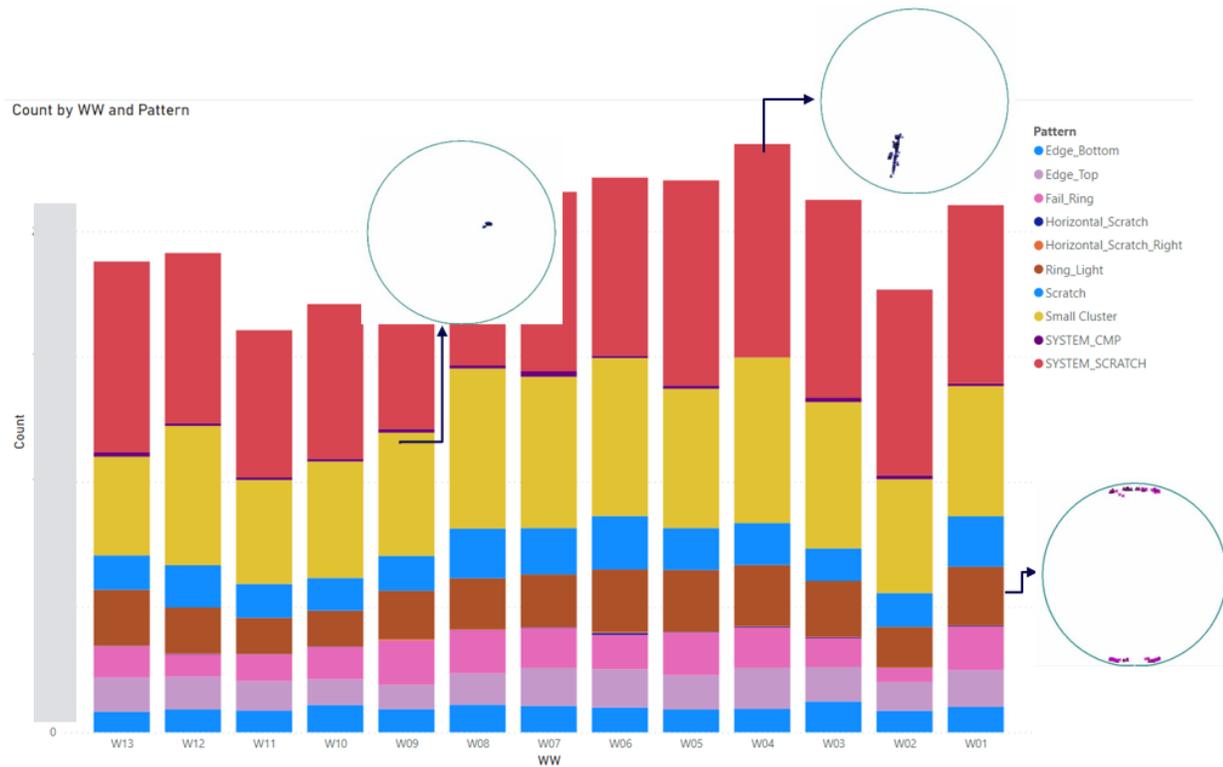
- An UNKNOWN pattern is an identified spatial signature that does not match an existing pattern in the production database



- Understand top-n high impacting new patterns that start to emerge & go unnoticed
- Efficiently maintain comprehensive Pattern Library
- Proactive response to production issues

Pattern Pareto based on hundreds of feature vectors

Results of learning from the “UNKNOWN” Patterns

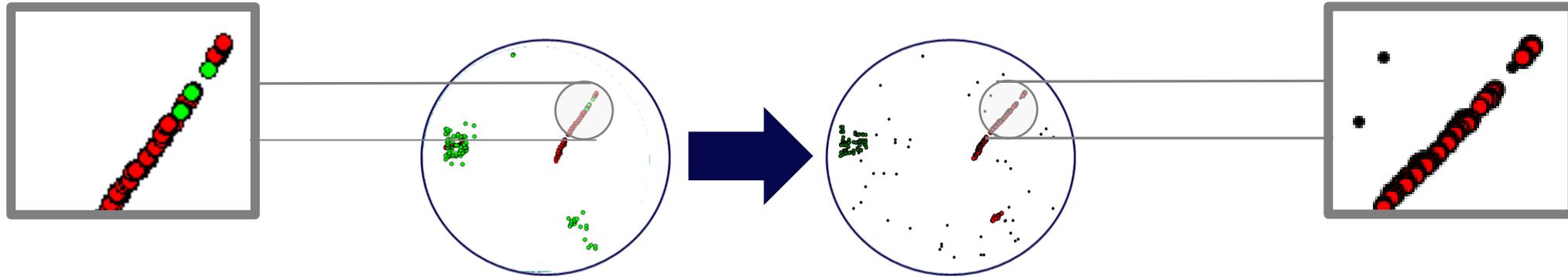


Pattern Pareto based on hundreds of feature vectors from UNKNOWN Patterns over 3 months

UNKNOWN Patterns tracked weekly

- Three key patterns were discovered: System Scratch, Small Cluster, and Ring Light.
- Analysis shows the potential for hundreds of wafers to be classified and correlated to yield limiting spatial issues.

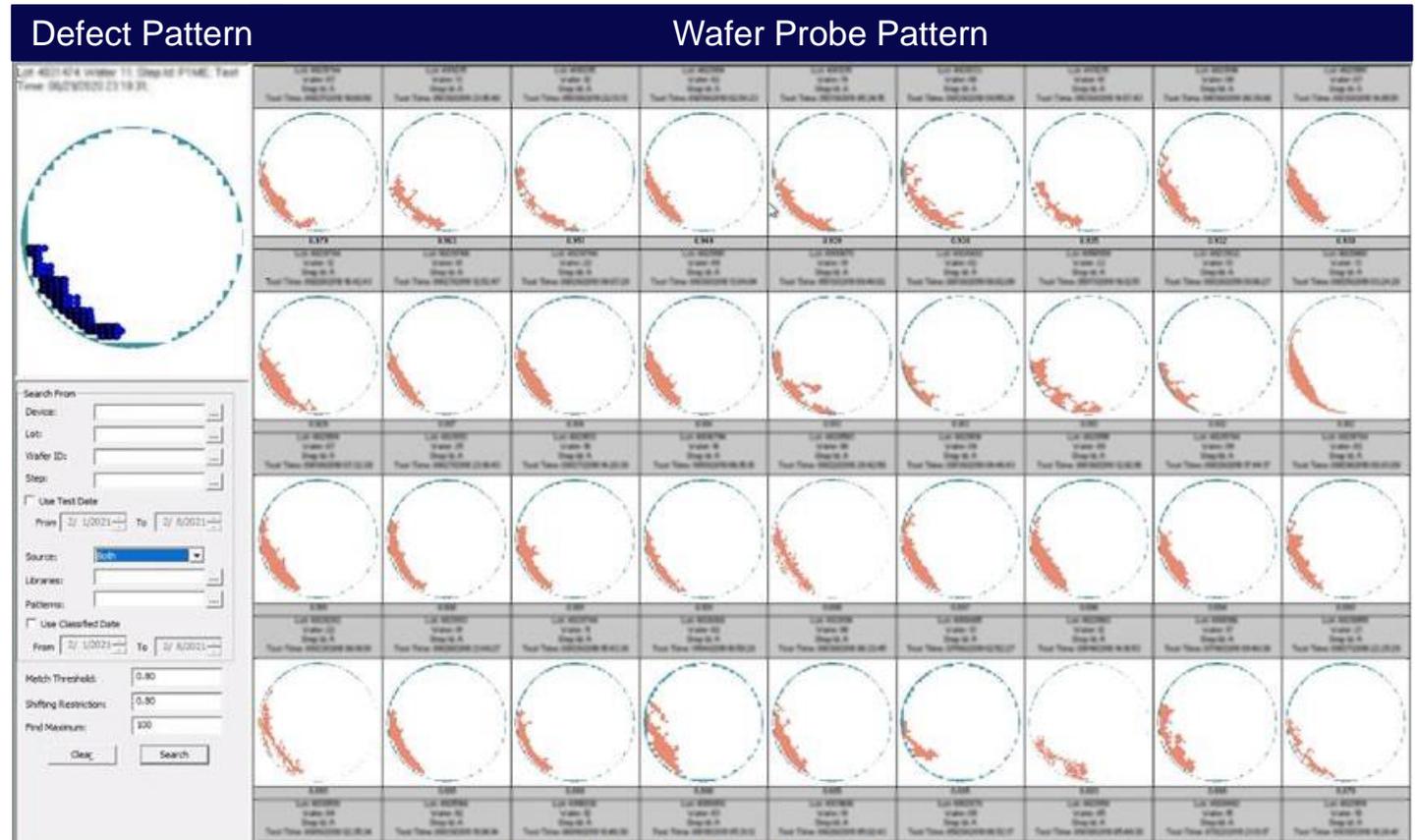
Challenges with Scratch Detection



Additional post processing algorithm is dedicated to running image-based scratch detection instead of defect-based detection using dynamic threshold determination (based on density and distribution) to eliminate or reduce false positives.

Case Study #1: Pattern Search Technique Improves Quality

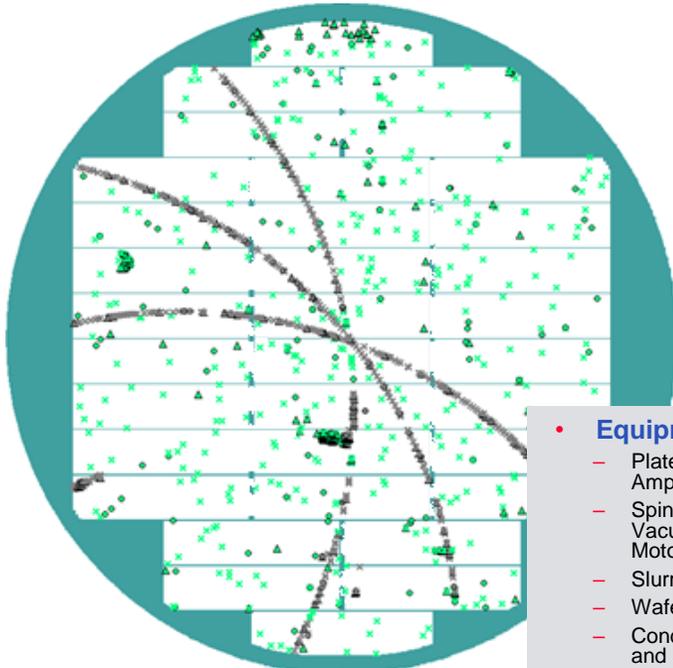
- Contributes to proactive analysis and build the Frequent Pattern Database
- Defect edge band pattern was used as reference to pull top 50 similar wafer probe patterns
- This powerful tool explicitly defines inline to EOL pattern commonalities and can be used to obtain accurate kill ratios



Case Study #2: SPR Identifies Root Cause Tool in CMP Excursion

Yield excursion linked to a recurring scratch formation in the CMP process.

Production limitations & low defect density impacts quality & contributes to scrap



CMP SCRATCH EXAMPLE

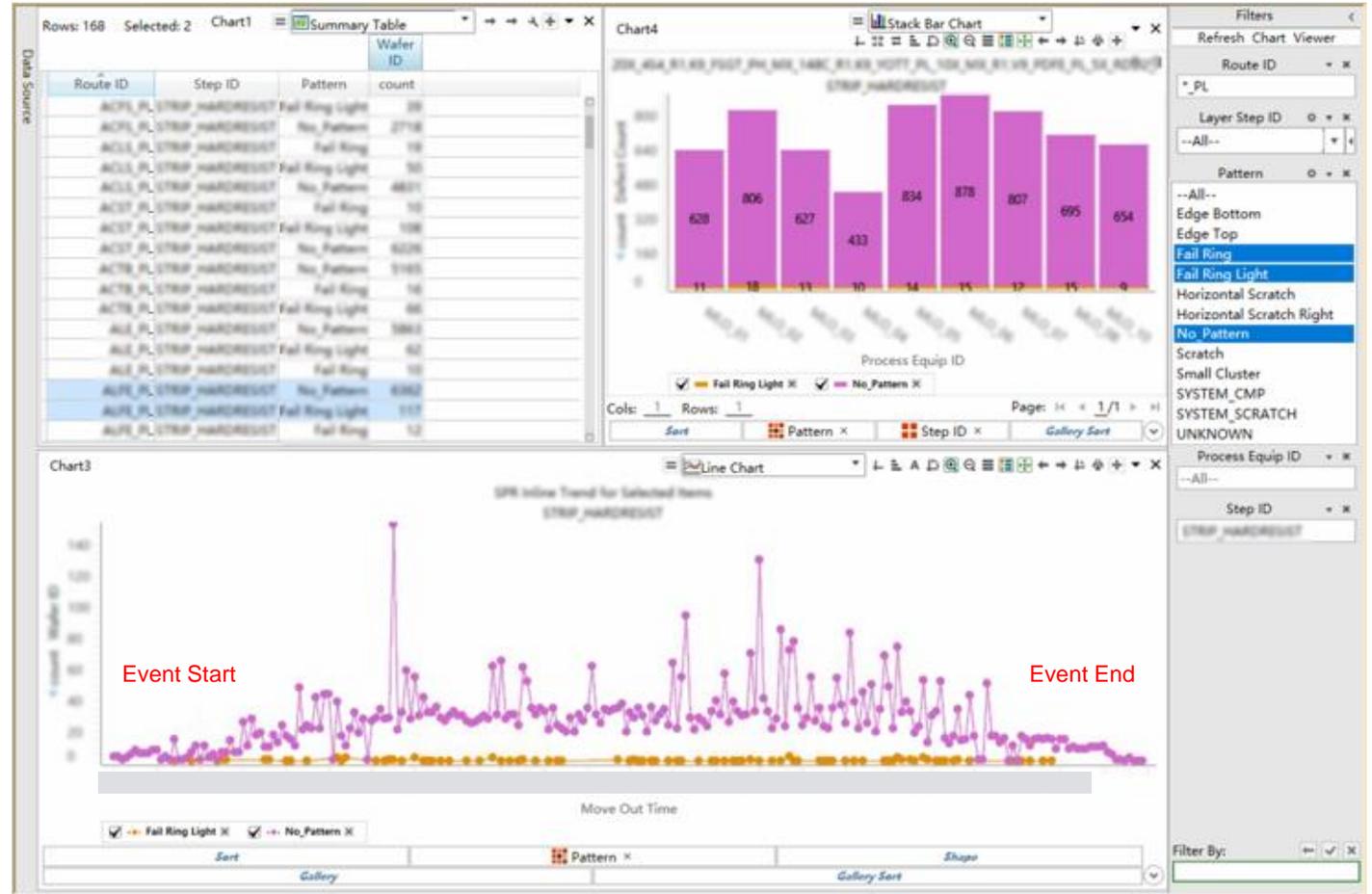
- **Equipment Data Monitored**
 - Platen Temp, Rotation Speed, Motor Amps
 - Spindle Head Speed, Vacuum/Pressure, Down force and Motor Amps
 - Slurry Dispense
 - Wafer Chuck Placement/Retry
 - Conditioner Sweep Speed, Motor Amps and Arm Force
 - Processing time (by platen)
 - Wet Idle Time
 - DI Water Flow
 - ...



Integration of this signature into the Frequent Pattern Database quickly highlights the root cause tool and event timeline.

Case Study #3: Killer Pattern Excursions by Process Tool & Date

- Chemical dispersion challenges at a strip step cause semi-circular ring like pattern
- Application of real-time alarm monitoring will notify the engineer and maintenance staff of this yield-impacting defect pattern, allowing for quick reaction times
- This known failure mode requires timely detection so wafer can be reworked to avoid scrap

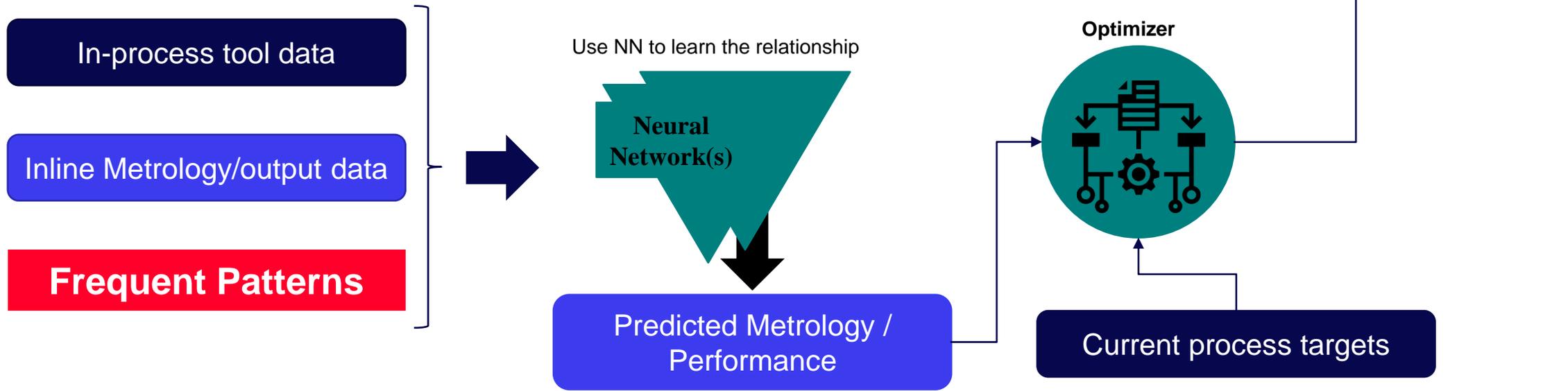
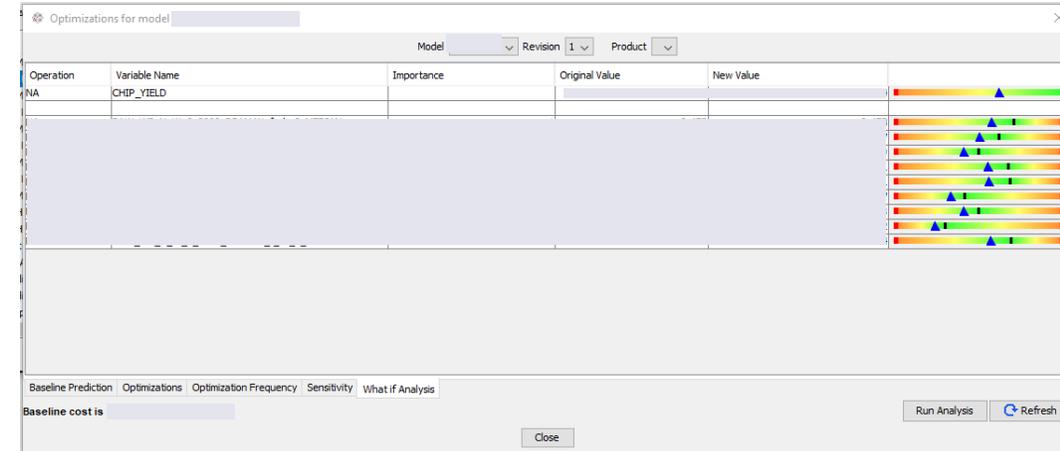


Trend split by Tool & Pattern, helps to find signal which would have been missed otherwise

Yield Improvement / Line Control

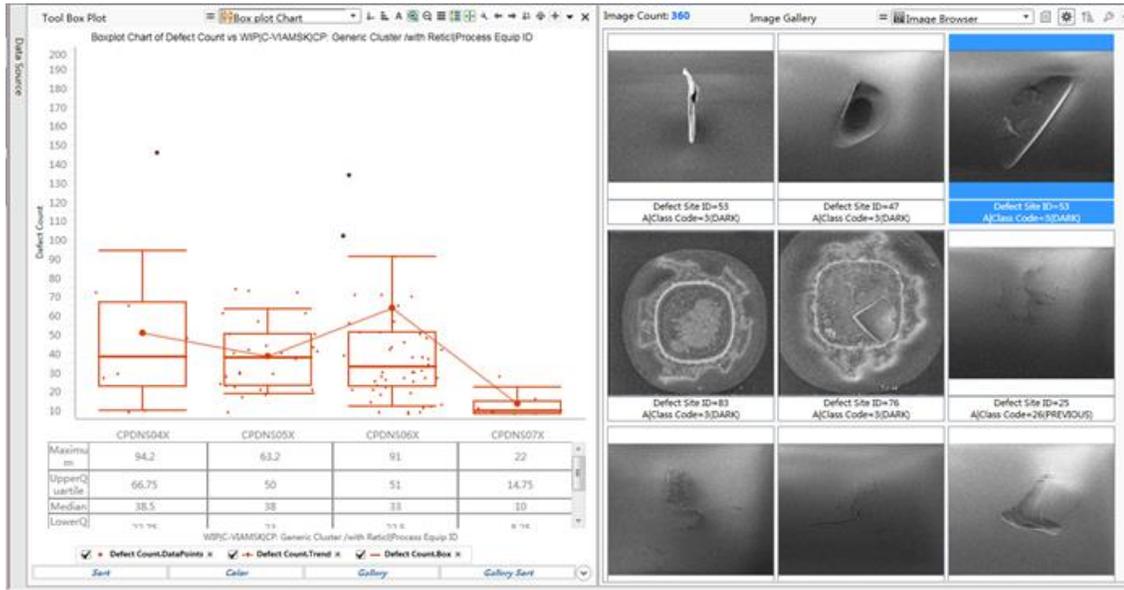
Yield Optimizer

- Analyze the relationships between in-process data and End-of-Line test or output results
- Recommend changes to the in-process targets to optimize the yield/output parameters



Improve Production Efficiency – React Faster & Reduce Scrap

Monitoring Dashboards



Process Event Reports

WIP: 22 Selected: 9

Daily Process Event Report

		Pattern																Score					
		MasterLibrary																					
		Pinwheel		Litho_partial		Litho_Stripping		Nuisance		SAGD_Band		Scratch3		SYSTEM_REPEAT_ER		TIN5_Clusters		UNKNOWN					
		> 0		> 0		> 0		> 0		> 0		> 0		> 0		> 0							
Seq	Route ID	Step ID	Process Equip ID	Count	%	Count	%	Count	%	Count	%	Count	%	Count	%	Count	%	Count	%				
6				20.69	3	10.34	3	10.34	2	6.9	9	31.03	8	27.59	1	3.45	7	24.14	7	24.14	22	75.86	
5				29.41	1	5.88	3	17.65	4	23.53	3	17.65	4	23.53	0	0	2	11.76	2	11.76	12	70.59	
5				26.32	0	0	6	31.58	4	21.05	4	21.05	5	26.32	0	0	3	15.79	3	15.79	14	73.68	
4				15.38	3	11.54	3	11.54	2	7.69	7	26.92	8	30.77	1	3.85	6	23.08	6	23.08	19	73.08	
4				33.33	1	8.33	3	25	4	33.33	3	25	1	8.33	0	0	1	8.33	3	25	10	83.33	
3				16.67	1	5.56	6	33.33	5	27.78	2	11.11	5	27.78	0	0	1	5.56	3	16.67	13	72.22	
3				12	4	16	6	24	5	20	3	12	5	20	0	0	2	8	4	16	17	68	
3				12	4	16	6	24	5	20	3	12	5	20	0	0	2	8	4	16	17	68	
3				15.79	4	21.05	0	0	2	10.53	6	31.58	4	21.05	1	5.26	4	21.05	6	31.58	15	78.95	
2				66.67	0	0	0	0	0	0	2	66.67	0	0	0	0	1	33.33	1	33.33	3	100	
2				22.22	1	11.11	3	33.33	4	44.44	1	11.11	1	11.11	0	0	0	0	2	22.22	7	77.78	
1				11.11	0	0	3	33.33	1	11.11	1	11.11	4	44.44	0	0	1	11.11	1	11.11	6	66.67	
1				10	0	0	3	30	1	10	1	10	4	40	0	0	1	10	1	10	6	60	
1				4.76	3	14.29	6	28.57	5	23.81	2	9.52	5	23.81	0	0	2	9.52	3	14.29	15	71.43	
1				5.56	3	16.67	3	16.67	2	11.11	5	27.78	5	27.78	1	5.56	4	22.22	6	33.33	14	77.78	
1				6.25	0	0	6	37.5	5	31.25	1	6.25	5	31.25	0	0	1	6.25	2	12.5	11	68.75	
0				0	0	0	0	0	2	100	0	0	0	0	0	0	0	0	0	0	2	100	
0				0	0	0	3	60	4	80	0	0	1	20	0	0	0	0	1	20	5	100	
0				0	0	3	42.86	0	0	0	0	1	14.29	0	0	0	0	1	14.29	1	14.29	4	57.14
0				0	0	3	42.86	0	0	0	0	1	14.29	0	0	0	0	1	14.29	1	14.29	4	57.14
0				0	0	3	42.86	0	0	0	0	1	14.29	0	0	0	0	1	14.29	1	14.29	4	57.14
0				0	0	3	27.27	3	27.27	2	18.18	1	9.09	1	9.09	0	0	1	9.09	2	18.18	7	63.64

Daily Pass Down Report to production highlights process tools that contribute to yield limiting alarm conditions

Engineering productivity improves ~25% by linking Pattern based Equipment Study with AOI data

Conclusions & Future Work

AI based Frequent Patterns Implementation

- Good: Key application
 - Shutdown faulty tools
 - Outline excursion scope
- Better: Reactive Analysis
 - Expedient association of wafer spatial signatures to process step & tool
- Best: Proactive Yield Management
 - Build Frequent Pattern Database
 - Avenue for proactive process control, yield enhancement & fab productivity

Continuous Improvement

- Discover Patterns on Metrology Data
- Whole wafer image-based Pattern Classification
- Frequent Patterns input to Yield Optimizer for Process Optimization

Acknowledgements



Prasad Bachiraju



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Thank You

谢谢 | 謝謝

Danke

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감사합니다

Obrigado

Merci

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